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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2665

Applicant: Dominic J. Gasbarro et al.

Title: HOST-FABRIC ADAPTER HAVING BANDWIDTH-OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED MEMORY ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCH FABRIC IN A DATA NETWORK

Docket No.: 884.969US1

Filed: December 8, 2000

Examiner: Unknown

Serial No.: 09/731,746

Due Date: N/A

Group Art Unit: Unknown

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450



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Technology Center 2600

We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
- A Communication Concerning Related Applications (2 pgs.).
- A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 17 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Ann M. McCrackin

Atty: Ann M. McCrackin

Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 17 day of September, 2004.

Name

KACIA LEE

Signature

Kacia Lee

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

S/N 09/731,746



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dominic J. Gasbarro et al. Examiner: Unknown
Serial No.: 09/731,746 Group Art Unit: Unknown
Filed: December 8, 2000 Docket: 884.969US1
Title: HOST-FABRIC ADAPTER HAVING BANDWIDTH-OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED MEMORY ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCH FABRIC IN A DATA NETWORK
Assignee: Intel Corporation Customer No.: 21186

COMMUNICATION CONCERNING RELATED APPLICATIONS **RECEIVED**

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/671707	September 28, 2000	884.968US1	HOST-FABRIC ADAPTER AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC IN A DATA NETWORK
09/669670	September 26, 2000	884.971US1	HOST-FABRIC ADAPTER AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC IN A DATA NETWORK
09/698166	October 30, 2000	884.982US1	HOST-FABRIC ADAPTER HAVING MEMORY ACCESS BANDWIDTH OPTIMIZING SMART DECODER
09/698188	October 30, 2000	884.983US1	HOST-FABRIC ADAPTER HAVING HARDWARE ASSIST ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC IN A DATA NETWORK

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 09/731,746

Filing Date: December 8, 2000

Title: HOST-FABRIC ADAPTER HAVING BANDWIDTH-OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED MEMORY ARCHITECTURE
AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCH FABRIC IN A DATA NETWORK

Assignee: Intel Corporation

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Dkt: 884.969US1 (INTEL)

09/672711	September 29, 2000	884.984US1	HOST-FABRIC ADAPTER HAVING HARDWARE ASSIST ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC IN A DATA NETWORK
09/671703 6775719	September 28, 2000	884.985US1	HOST-FABRIC ADAPTER AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC IN A DATA NETWORK

Respectfully submitted,

DOMINIC J. GASBARRO ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLÜTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9592

Date Sept. 16, 2004 By Ann M. McCrackin
Ann M. McCrackin
Reg. No. 42,858

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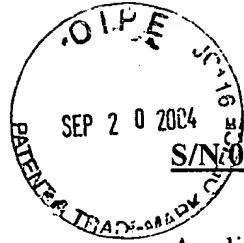
September, 2004.

Name

KACIA LEE

Signature

Kacia Lee



SEP 20 2004

S/N 09/731,746

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Dominic J. Gasbarro et al.	Examiner:	Unknown
Serial No.:	09/731,746	Group Art Unit:	Unknown
Filed:	December 08, 2000	Docket:	884.969US1
Title:	HOST-FABRIC ADAPTER HAVING BANDWIDTH-OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED MEMORY ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCH FABRIC IN A DATA NETWORK		
Assignee:	Intel Corporation	Customer No:	21186

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2600

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No : 09/731,746

Filing Date: December 08, 2000

Title: HOST-FABRIC ADAPTER HAVING BANDWIDTH-OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED MEMORY ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-BASED SWITCH FABRIC IN A DATA NETWORK

Assignee: Intel Corporation

Page 2

Dkt: 884.969US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DOMINIC J. GASBARRO ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, Minnesota 55402
(612) 349-9592

Date Sept. 16, 2004 By Ann M. McCrackin
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Kacia Lee
Name

Kacia Lee
Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/731,746
Filing Date	December 8, 2000
First Named Inventor	Gasbarro, Dominic J.
Group Art Unit	Unknown
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 884.969US1 Technology Center 2600

RECEIVED**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-2001/0053148-A1	12/20/2001	Billic, H. , et al.	370	389	03/16/2001
	US-2002/0071450	06/13/2002	Gasbarro, Dominic J., et al.	370	463	12/08/2000
	US-2003/0070014-A1	04/10/2003	Haren, K. C.	710	65	09/26/2002
	US-5,634,015	05/27/1997	Chang, P. , et al.	395	309	10/04/1994
	US-6,188,690	02/13/2001	Holden, B. D., et al.	370	390	12/11/1997
	US-6,243,787	06/05/2001	Kagan, M. , et al.	710	263	04/27/2000
	US-6,400,730	06/04/2002	Latif, A. , et al.	370	466	02/08/2000
	US-6,421,711	07/16/2002	Blumenau, S. M., et al.	709	213	06/29/1998
	US-6,545,981	04/08/2003	Garcia, D. J., et al.	370	242	12/30/1998
	US-6,557,060	04/29/2003	Haren, K. C.	710	65	04/25/2000
	US-6,591,310	07/08/2003	Johnson, S. B.	710	3	05/11/2000
	US-6,594,701	07/15/2003	Forin, A.	709	232	12/31/1998
	US-6,668,299	12/23/2003	Kagan, M. , et al.	710	305	09/06/2000
	US-6,678,782	01/13/2004	Aydemir, M , et al.	710	316	06/27/2000
	US-6,690,757	02/10/2004	Bunton, W. P., et al.	375	371	06/20/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Next Generation I/O Link Architecture Specification: HCA Specification", NGIO Forum, Release 1.0.0, (July 20, 1999), 1-58	
		"Virtual Interface Architecture Specification", Draft Revision 1.0, (December 4, 1997), 1-83	

EXAMINER**DATE CONSIDERED**